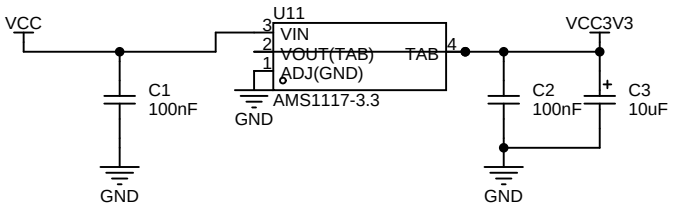
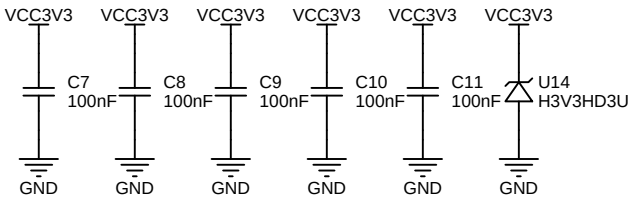
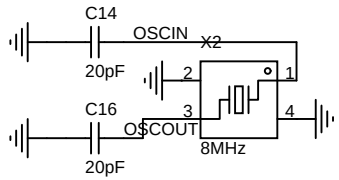
5V-3.3V

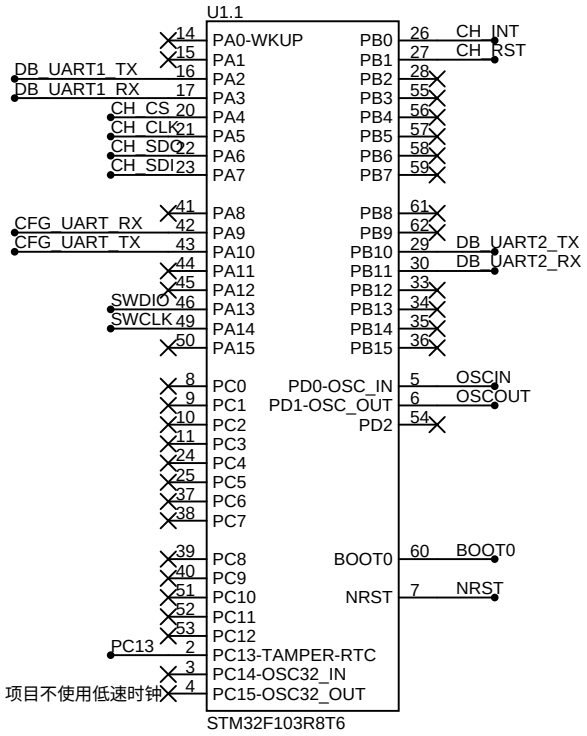


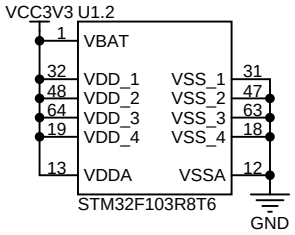
滤波与保护



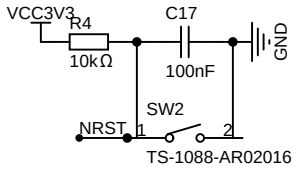
主时钟



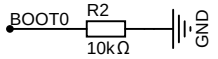




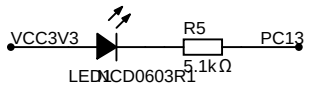
复位



BOOT



指示灯



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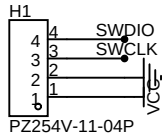
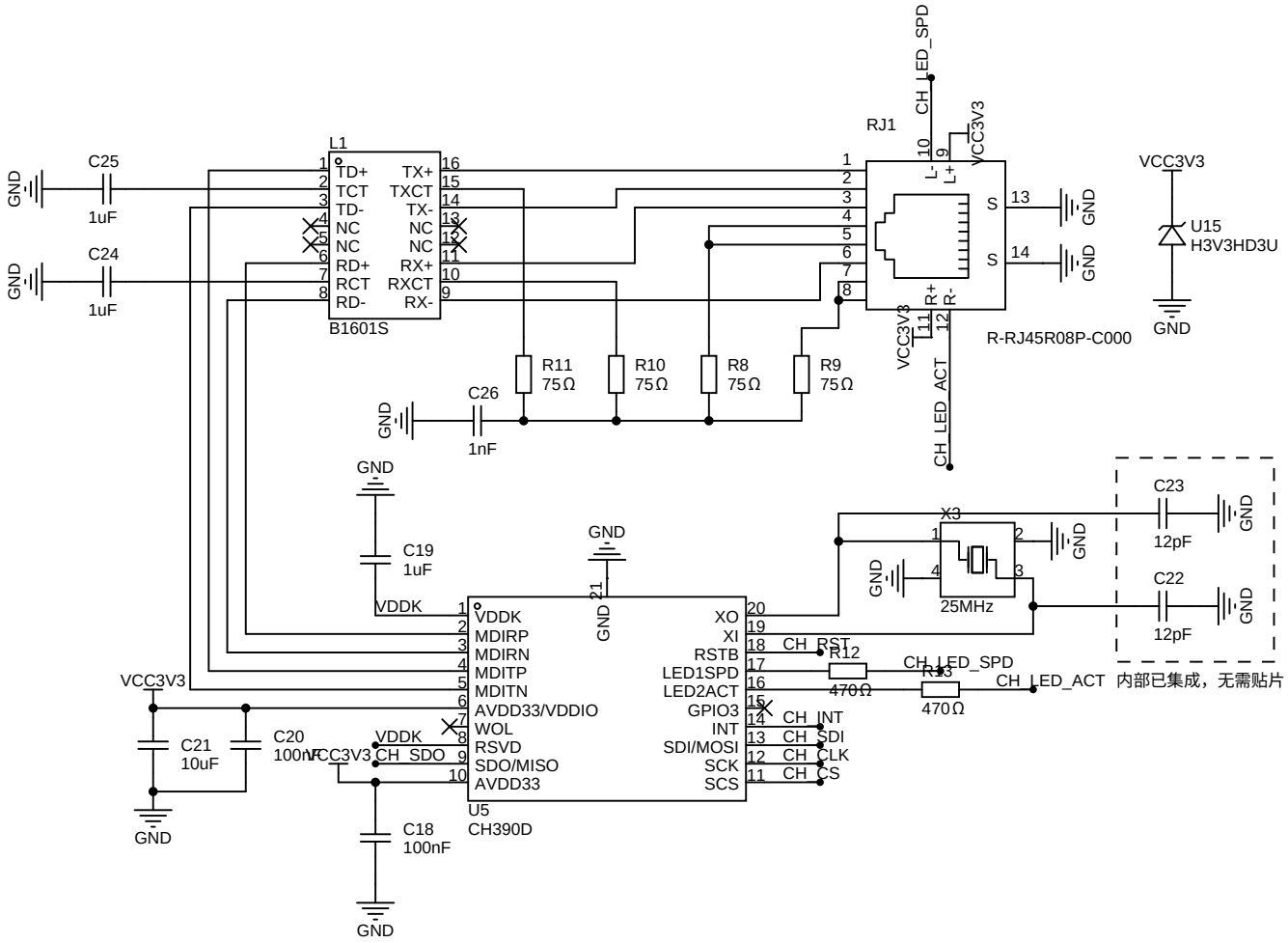


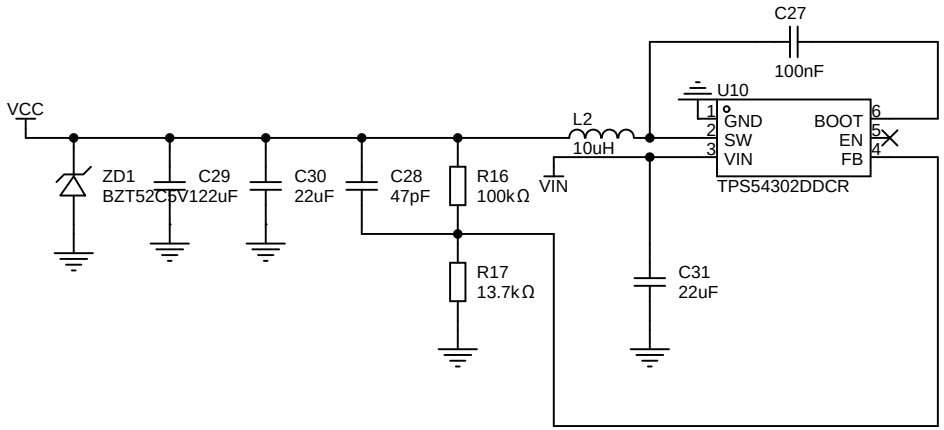
原理图	Schematic1	创建日期	2026-03-23
		更新日期	2026-03-23
板子	TCP2UART	图页	STM32F103R8T6Core
绘制		TCP2UART	
审阅			
		版本	尺寸	页 1 共 4
嘉立创EDA		V1.0	A4	嘉立创EDA

网口



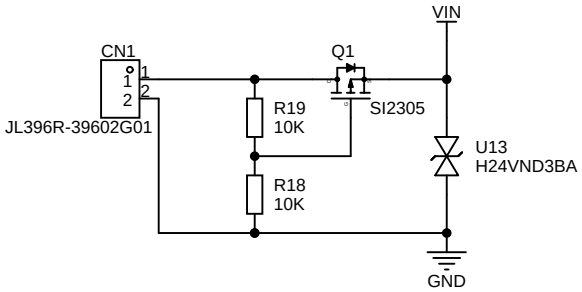
原理图	Schematic1	创建日期	2026-03-23
		更新日期	2026-03-23
板子	TCP2UART	图页	CH390D
绘制		TCP2UART	
审阅			
		版本	尺寸	页 2 共 4
嘉立创EDA		V1.0	A4	嘉立创EDA

12V-5V

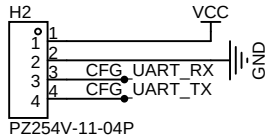


原理图	Schematic1	创建日期	2026-03-23
		更新日期	2026-03-23
板子	TCP2UART	图页	TPS54302
绘制		TCP2UART	
审阅			
		版本	尺寸	页 3 共 4
嘉立创EDA		V1.0	A4	嘉立创EDA

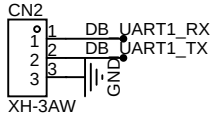
防反接与ESD



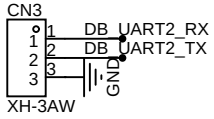
配置串口



透传口1



透传口2



原理图	Schematic1	创建日期	2026-03-23
		更新日期	2026-03-24
板子	TCP2UART	图页	I0
绘制		TCP2UART	
审阅			
		版本	尺寸	页 4 共 4
嘉立创EDA		V1.0	A4	嘉立创EDA